

<u>DB Name</u>	<u>Query</u>	<u>Hit Count</u>	<u>Set Name</u>
USPT,PGPB,JPAB,EPAB,DWPI,TDBD	(programmable adj (logic or gate) adj array) same l21	18	<u>L25</u>
USPT,PGPB,JPAB,EPAB,DWPI,TDBD	(pla or fpla or fpga or pga) same l21	21	<u>L24</u>
USPT,PGPB,JPAB,EPAB,DWPI,TDBD	l21 with l2	0	<u>L23</u>
USPT,PGPB,JPAB,EPAB,DWPI,TDBD	l21 and l17	1	<u>L22</u>
USPT,PGPB,JPAB,EPAB,DWPI,TDBD	l3 near5 (program\$5 or configur\$5)	2258	<u>L21</u>
USPT,PGPB,JPAB,EPAB,DWPI,TDBD	l3 and l19	7	<u>L20</u>
USPT,PGPB,JPAB,EPAB,DWPI,TDBD	l11 and l17	138	<u>L19</u>
USPT,PGPB,JPAB,EPAB,DWPI,TDBD	l17 and l12	1	<u>L18</u>
USPT,PGPB,JPAB,EPAB,DWPI,TDBD	((716/16)!.CCLS.)	173	<u>L17</u>
USPT,PGPB,JPAB,EPAB,DWPI,TDBD	l3 and l12	39	<u>L16</u>
USPT,PGPB,JPAB,EPAB,DWPI,TDBD	l3 and l11	1527	<u>L15</u>
USPT,PGPB,JPAB,EPAB,DWPI,TDBD	l1 same l12	0	<u>L14</u>
USPT,PGPB,JPAB,EPAB,DWPI,TDBD	l3 same l12	4	<u>L13</u>
USPT,PGPB,JPAB,EPAB,DWPI,TDBD	l5 with l11	107	<u>L12</u>
USPT,PGPB,JPAB,EPAB,DWPI,TDBD	l9 or l10	25337	<u>L11</u>
USPT,PGPB,JPAB,EPAB,DWPI,TDBD	configur\$5 adj2 logic	3552	<u>L10</u>
USPT,PGPB,JPAB,EPAB,DWPI,TDBD	(program or programming or programmable)- adj2 logic	23216	<u>L9</u>
USPT,PGPB,JPAB,EPAB,DWPI,TDBD	l3 same l7	0	<u>L8</u>
USPT,PGPB,JPAB,EPAB,DWPI,TDBD	bus with l2	69	<u>L7</u>
USPT,PGPB,JPAB,EPAB,DWPI,TDBD	l5 with l2	0	<u>L6</u>
USPT,PGPB,JPAB,EPAB,DWPI,TDBD	system adj bus	24202	<u>L5</u>
USPT,PGPB,JPAB,EPAB,DWPI,TDBD	l2 same l1	4	<u>L4</u>
USPT,PGPB,JPAB,EPAB,DWPI,TDBD	dma or dmac or (direct memory access\$3)	27964	<u>L3</u>
USPT,PGPB,JPAB,EPAB,DWPI,TDBD	(configur\$4 adj system adj logic) or csl	1940	<u>L2</u>
USPT,PGPB,JPAB,EPAB,DWPI,TDBD	(system adj2 chip) or soc or csoc	135480	<u>L1</u>

5/15/01